

FIG. 1

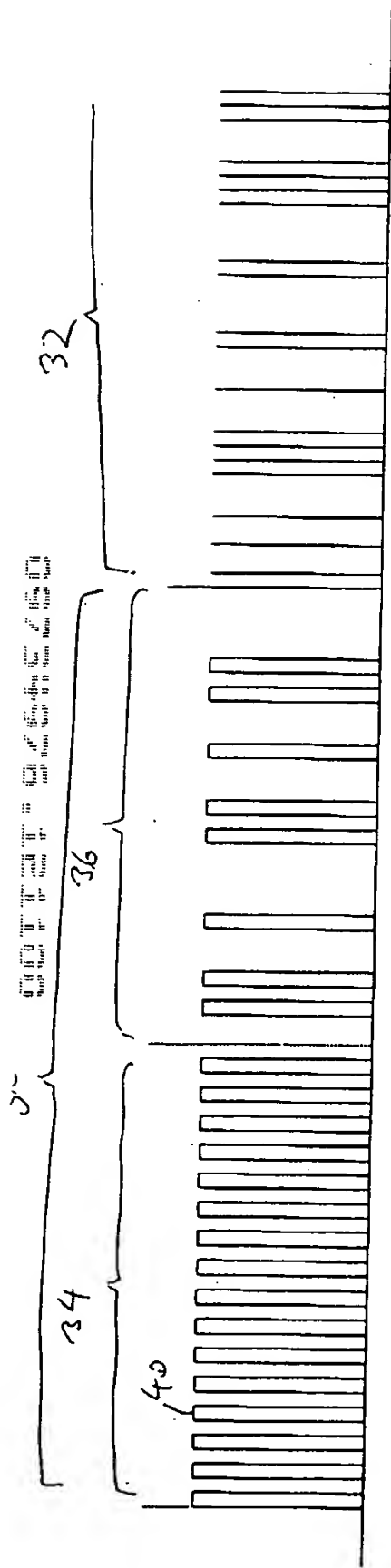


Fig 2

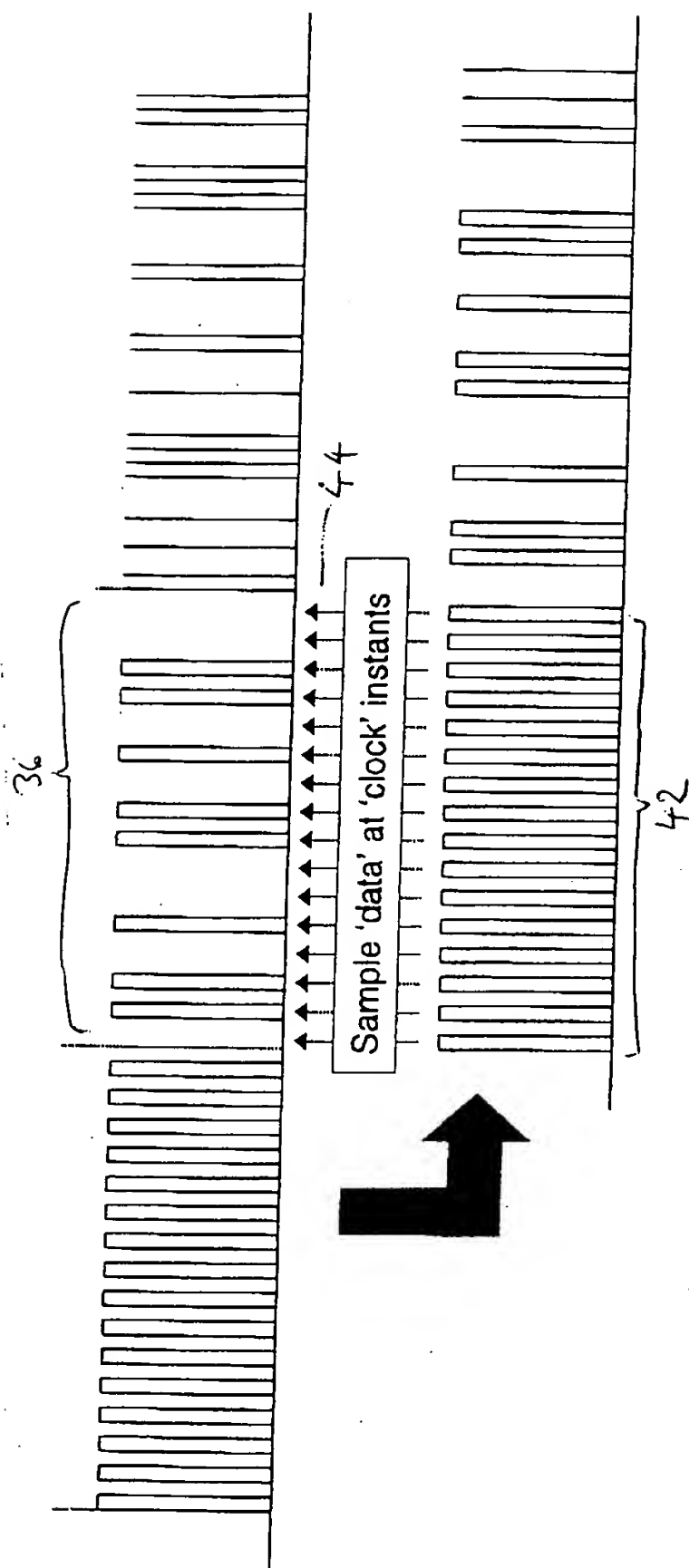


Fig 3

FIG. 4 is a block diagram of a digital signal processing system. The system includes a digital filter 34, a digital-to-analog converter 36, and an analog-to-digital converter 38. The digital filter 34 receives a digital input signal 32 and outputs a digital signal 40. The digital-to-analog converter 36 converts the digital signal 40 into an analog signal 42. The analog-to-digital converter 38 converts the analog signal 42 into a digital signal 44. The digital signal 44 is then fed back into the digital filter 34. The system also includes a clock signal 46, which is used to synchronize the operations of the digital filter 34, the digital-to-analog converter 36, and the analog-to-digital converter 38. The clock signal 46 is generated by a clock source 48. The digital filter 34 is implemented using a series of delay elements 50 and adders 52. The digital-to-analog converter 36 is implemented using a series of digital-to-analog converters 54. The analog-to-digital converter 38 is implemented using a series of analog-to-digital converters 56. The system is configured to process a digital input signal 32 and produce a digital output signal 44. The system is also configured to process an analog input signal 42 and produce a digital output signal 44. The system is further configured to process a digital input signal 32 and produce an analog output signal 42. The system is also configured to process an analog input signal 42 and produce an analog output signal 42. The system is configured to process a digital input signal 32 and produce a digital output signal 44. The system is also configured to process an analog input signal 42 and produce a digital output signal 44. The system is further configured to process a digital input signal 32 and produce an analog output signal 42. The system is also configured to process an analog input signal 42 and produce an analog output signal 42.

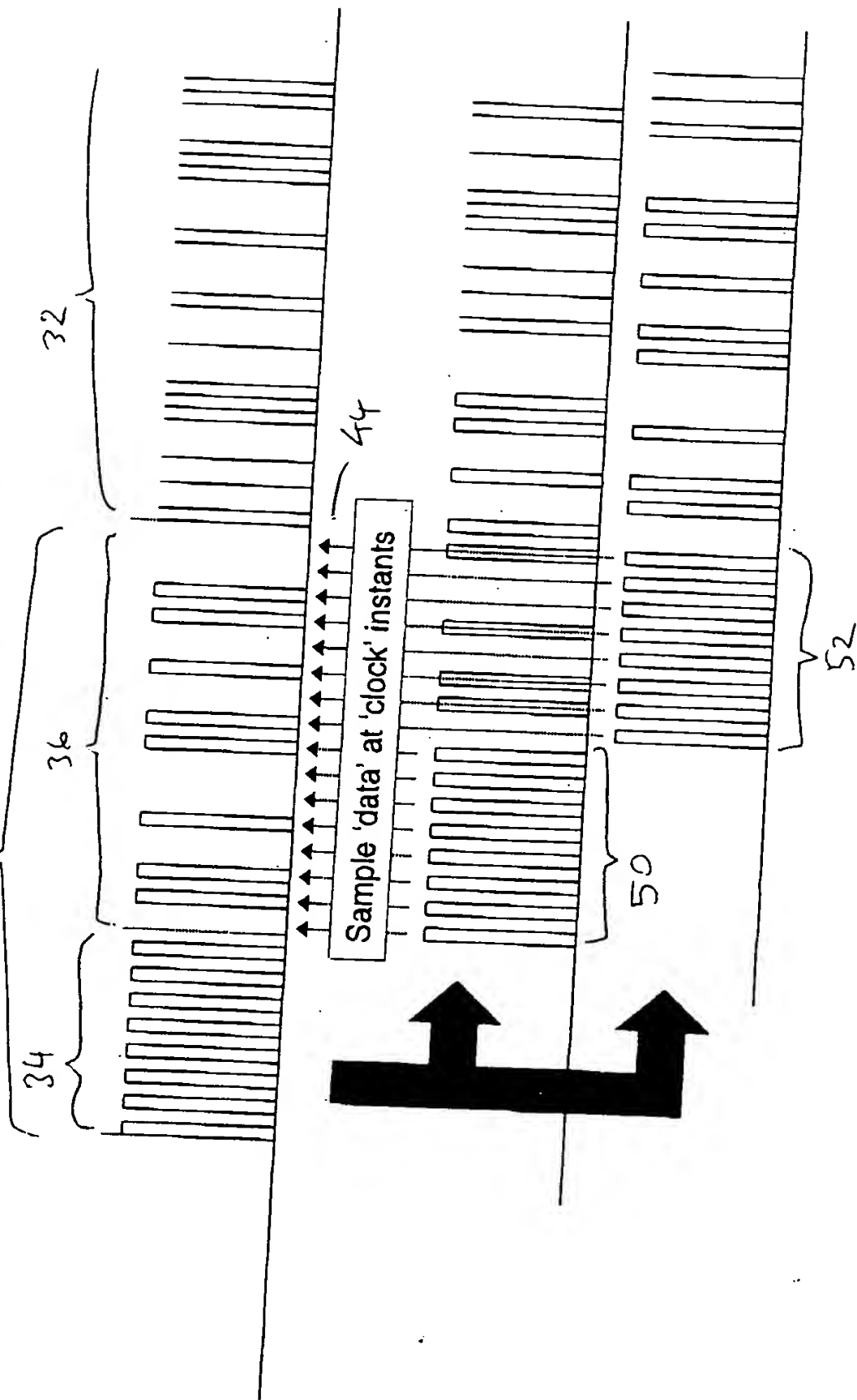


Fig 4

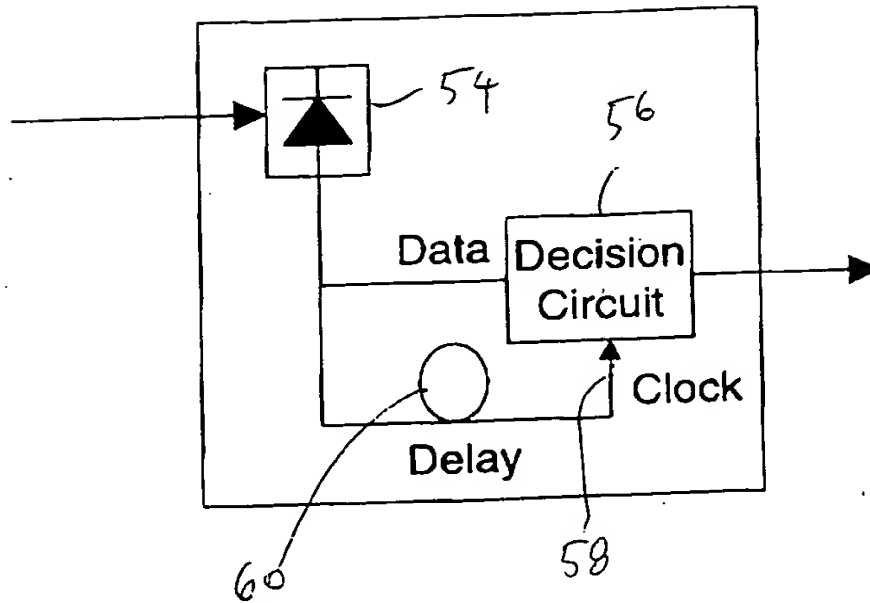


FIG 5

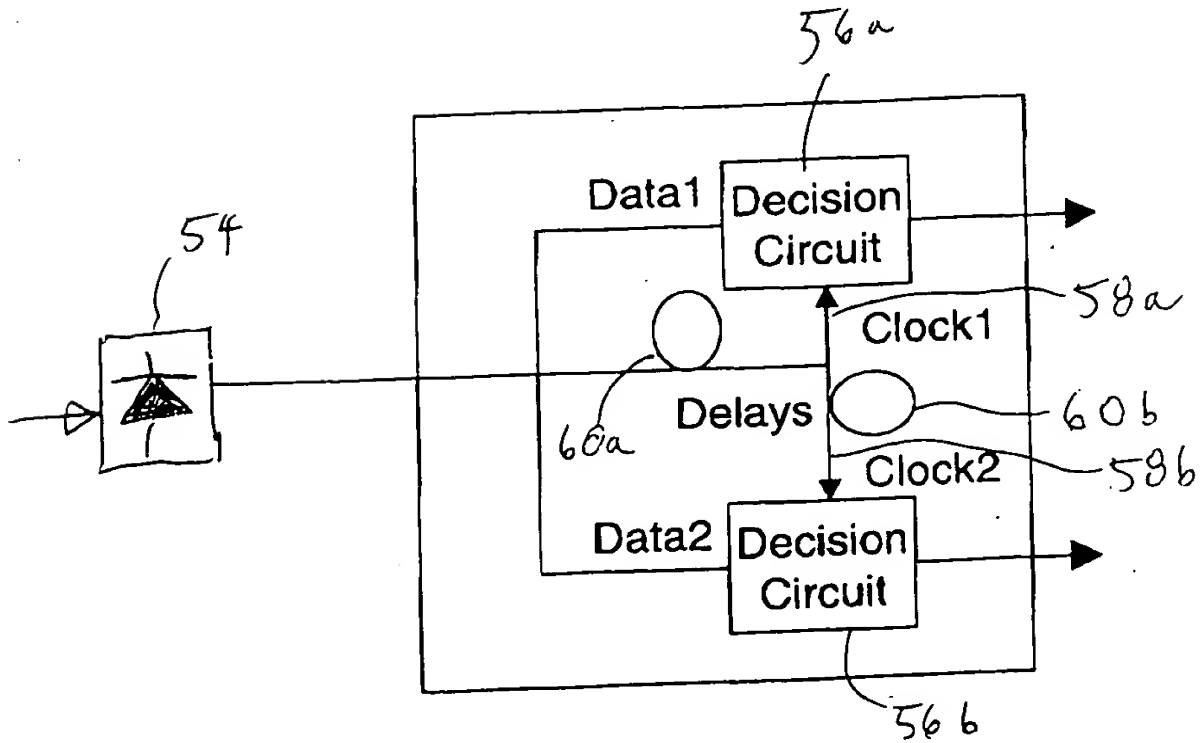


FIG 6

FIG. 7 is a schematic diagram of a system for controlling a vehicle, and FIG. 8 is a schematic diagram of a system for controlling a vehicle.

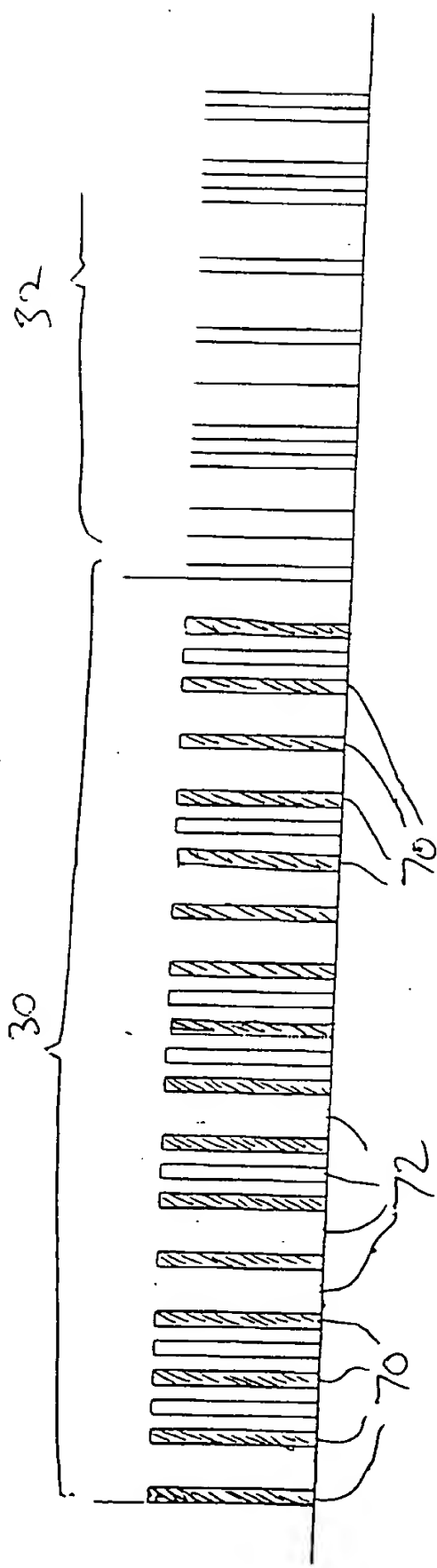


Fig 7

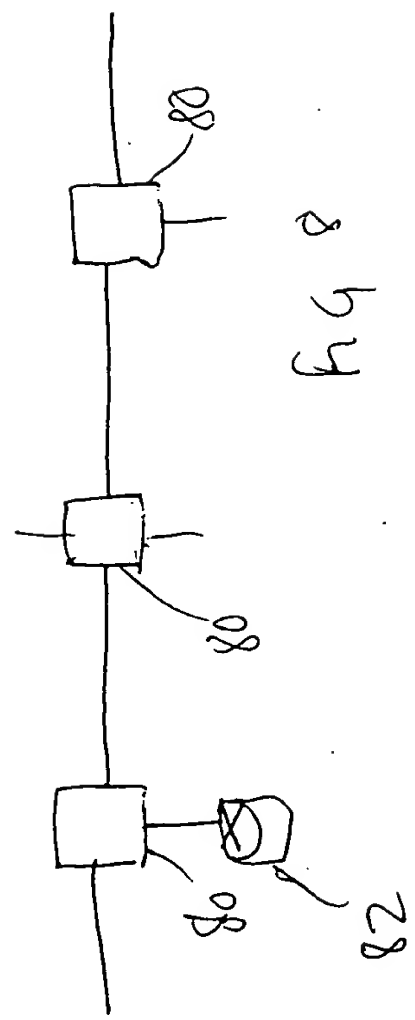


Fig 8